151. (Amended)   A method of controlling a memory device by a memory controller, wherein the memory device includes a plurality of memory cells, the method of controlling the memory device comprises:

   providing first block size information to the memory device, wherein the first block size information is provided by the memory controller and defines a first amount of data to be input by the memory device in response to a write request; and

   issuing a first write request to the memory device, wherein in response to the first write request the memory device inputs the first amount of data corresponding to the first block size information.

171. (Twice Amended)   A method of operation of an integrated circuit, wherein the integrated circuit includes a memory array having a plurality of memory cells, the method of operation comprises:

   receiving block size information from a controller, wherein the block size information defines a first amount of data to be input in response to a write request;

   receiving a first write request; and

   inputting the first amount of data corresponding to the block size information in response to the first write request.

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